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Transmitted herewith for filing is the Patent Application of:

Inventor: **RICHARD BULLOCK AND DAVID PAUL JONES**

For: METHOD OF FABRICATING A GATE DIELECTRIC LAYER FOR A THIN FILM TRANSISTOR

31031 U.S. PTO
09/773872
02/02/01

Enclosed are:

☒ 3 sheets of drawing(s) - formal.

☐ An assignment of the invention to **ESM Limited**

☐ An associate power of attorney ☐ Applicant claims small entity status

☐ Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE
BASIC FEE				\$ 710.
TOTAL CLAIMS	15 -20=	0	x 18 =	\$ 0.
INDEP CLAIMS	2 -3=	0	x 80 =	\$ 0.
MULTIPLE DEPENDENT CLAIM PRESENTED			+ 260 =	
			SUB TOTAL	\$ 710.
			ASSIGNMENT	\$40.
			TOTAL	\$ 750.

☒ Please charge my Deposit Account No. 19-0033 in the amount of \$ 750. A duplicate copy of this sheet is enclosed.

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☒ Any additional filing fees required under 37 CFR \$1.16.

☒ Any patent application processing fees under 37 CFR \$1.17.

Respectfully submitted,

Stephen B. Ackerman
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